



**Dynamic Ram:** The capacitance of each Field Effect Transistor allows the storage of a charge. The transistor itself acts as a switch that allows the "memory" to be read. Over time, the charge drains away from the capacitor and hence needs to be continuously refreshed (several hundred times/sec) even if the power is on. Hence "dynamic". A 4 X 4 DRAM array.

A  
single DRAM cell

